



NEW!

FROM
**ANDERSEN
LABORATORIES**

20,000 BIT MEMORY UNIT

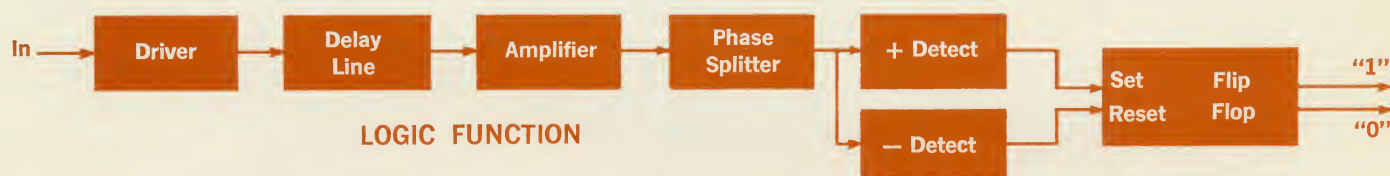
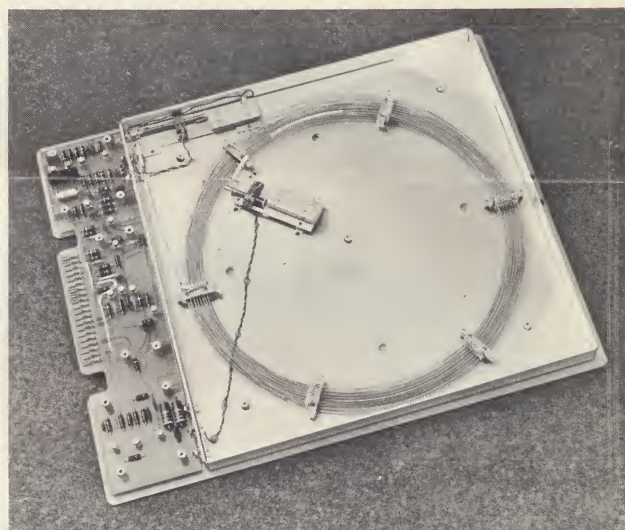
Anderson's 20,000 bit, 10,000 μ sec 2 Megacycle N R Z delay line has been designed to be used as a serial memory unit. It is a unity gain delay package that has less than 0.5 PPM/ $^{\circ}$ C delay stability over a temperature range from 0 $^{\circ}$ to 50 $^{\circ}$ C. It is constructed for easy installation in a standard relay rack. These units have been used in series and/or parallel to construct extremely large memories.

A functional description of our delay unit is as follows: An N R Z input signal is applied to the delay line driver. Logical "1" is represented by the more positive voltage level. The delay line driver converts the input to a current drive which is connected to the input transducer of the delay line. The delay line input transducer differentiates the input waveform as does the output transducer which results in a voltage doublet.

The video amplifier increases the output of the delay line to drive the phase splitter. The phase splitter supplies the proper polarity signal to the detectors.

The positive and negative detectors accept either positive or negative polarity voltages respectively and reject the opposite polarities. The detector outputs set and reset the flip-flop and in this manner recover the delayed information.

Flip-flop outputs are fed to emitter followers, which provide low impedance outputs for the system. The outputs from the emitter followers are clamped to the -6 volt power supply and ground assuring load insensitive output voltages.



SPECIFICATIONS

Delay — 10,000 μ s (up to 16,000 available)
Mode of Operation N.R.Z. (R.Z. and/or Bipolar available)
Repetition Rate — 2 mc NRZ

INPUT CHARACTERISTICS

Logical "1" 0 \pm 1 volt
Logical "0" -6 \pm 1 volt
Input Impedance 570 Ω min. in parallel with 75 μ f
Max. PRF 2 Mc NRZ
Max. Input Rise and Fall Times 80 nsec
Noise Rejection 1.5 volts from nominal signal levels.

OUTPUT CHARACTERISTICS

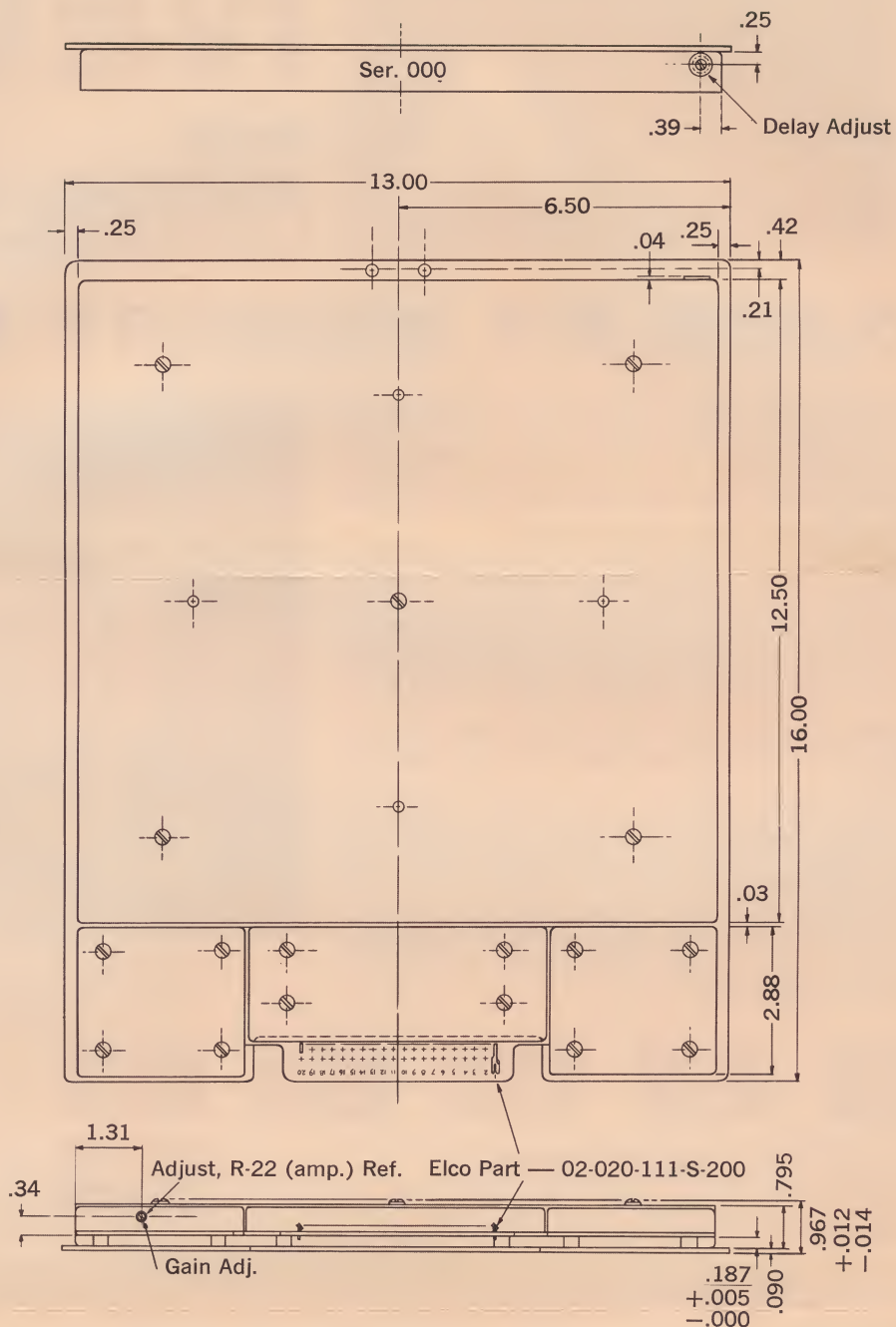
"0" and "1" Output available simultaneously.
Logical "1" 0 \pm 1 volt @ 15 ma max.
Logical "0" -5.5 \pm 0.5 volt @ 25 ma max.
Max. stray capacitance 300 μ f
Max. Rise and Fall Times into Full Load 100 nsec

SUPPLY VOLTAGES

-18 V \pm 5% @ 165 ma max.
- 6 V \pm 5% @ 30 ma (Reverse current)
+12 V \pm 5% @ 30 ma max.

ENVIRONMENT

Temperature — 0 to 50 $^{\circ}$ C
Shock — Bench Handling
MIL-S-901B — Available
Vibration — Commercial Handling
MIL-T-5422 — Available



20,000 BIT MEMORY UNIT

PRICE

1-10	285.00
11-25	260.00
26-50	245.00
51-100	235.00

Larger Quantity Quotation on Request

Delivery — 30 days

Prices & Specifications subject to change without notice.



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